



**AVN**

**INSTITUTE OF ENGINEERING & TECHNOLOGY**  
Accredited by NAAC & NBA  
Autonomous Institute Affiliated to JNTU Hyderabad



**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

ACADEMIC YEAR 2025-2026

**TIME TABLE(4)**

Class & Semester : III-B.Tech II- SEM ECE- A

Class Co-Ordinator: Mrs.A.G.Neethuram

w.e.f : 02.02.2026

Lecture Hall: B-202

DAY/ PERIOD	1	2	3	4	5	6	7	4:20-4:40
MONDAY	VLSI	DSP	AWP	MCN	VLSI	MCN	EIA	
TUESDAY	VLSI	←	AC(B1)/VLSI(B2) →	←	EIA	DSP	AWP	
WEDNESDAY	VLSI	DSP	AWP	MCN	←	←	←	
THURSDAY	←	←	←	←	←	←	←	
FRIDAY	AWP	DSP	MCN	←	←	←	←	
SATURDAY	EIA	←	DSP(B1)/AC(B2) →	←	MCN	←	←	

Subject Code	Subject Name	Name of the Faculty
EC309	Digital Signal Processing	Dr.N.Suresh
EC311	CMOS VLSI Design	Dr.G.Dhanalakshmi
EC313	Antennas and Wave Propagation	Mrs.M.Arpana
EC316	Mobile Communications and Networks(PE-2)	Ms.K.Soumya
OE02	Environmental Impact Assessment (OE-2)	Mr.Ravinder
EC310	Digital Signal Processing Lab	Mr.M.Ramesh, Mr.Govind Raj, Mrs.P.Priyanka
EC312	CMOS VLSI Design Lab	Mr.M.Sharath Chandra, Ms.T.Gayathri, Mr.K.Venkat
EC314	Advanced Communication Lab	Mrs.M.Arpana, Ms.K.Soumya, Mr.M.Venkataiah
EC319	Industry Oriented Mini Project/Internship	Mr.Ch.Sreedhar, Mrs.M.Arpana
	Library	Mr.M.Venkataiah/Ms.T.Gayathri

\*RC=Remedial Class

\*CRT=Campus Recruitment Training

*K. Suresh*  
TIME TABLE I/C

*N.K*  
HOD

Head of the Department  
 Electronics and Communication Engineering  
 AVN Institute of Engineering & Technology  
 Mangalpally (V) Patonguda (M)  
 Hyderabad - 500082, Telangana  
 Principal  
 am (M) R.R Dist

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING  
 ACADEMIC YEAR 2025-2026

Class & Semester : III-B.Tech II- SEM ECE- B  
 Class Co-Ordinator: Mr.Ch.Sundeep Kumar

TIME TABLE(4)

w.e.f : 02.02.2026  
 Lecture Hall: B- 203

DAY/ PERIOD	1 9:20-9:40	2 9:40-10:30	3 10:30-11:20	4 11:20-11:30	5 11:30-12:20	6 12:20-01:10	7 01:10-01:50	8 1:50-02:40	9 2:40-03:30	10 3:30-04:20	11 4:20-4:40
MONDAY	AWP	VLSI	MCN	DSP	MCN	DSP	EIA	VLSI	MCN	MCN	B I O M E T R I C
TUESDAY	AWP	VLSI	EIA	DSP	VLSI(B1)/DSP(B2)	AWP	MCN(RC)	AWP(RC)	DSP(RC)	MCN	B I O M E T R I C
WEDNESDAY	AWP	VLSI	EIA	DSP	VLSI(B1)/DSP(B2)	AWP	DSP	AWP	VLSI	MCN	B I O M E T R I C
THURSDAY	AWP	VLSI	EIA	DSP	VLSI(B1)/DSP(B2)	AWP	MCN(RC)	AWP(RC)	DSP(RC)	MCN	B I O M E T R I C
FRIDAY	AWP	VLSI	EIA	DSP	VLSI(B1)/DSP(B2)	AWP	DSP(RC)	VLSI(RC)	EIA(RC)	MCN	B I O M E T R I C
SATURDAY	AWP	VLSI	EIA	DSP	VLSI(B1)/DSP(B2)	AWP	DSP(RC)	VLSI(RC)	EIA(RC)	MCN	B I O M E T R I C

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EC310	Digital Signal Processing Lab	Mr.M.Ramesh,Mr.Govind Raj,Mrs.P.Priyanka
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EC319	Industry Oriented Mini Project/Internship	Mr.Ch.Sreedhar,Mrs.M.Arpana
	Library	Mr.M.Venkataiah/Ms.T.Gayathri

*K. S. S.*  
 TIME TABLE VC

HOD

Head of the Department AVN Institute of Engineering & Technology  
 Electronics and Communication Engineering  
 AVN Institute of Engineering & Technology  
 Mangalpet (V) Patejguda (MP),  
 Patejguda (V) Patejguda (MP),  
 Bangalore - 560075  
 am (M) R.R. Dist T.S.

PRINCIPAL